

Fig.1

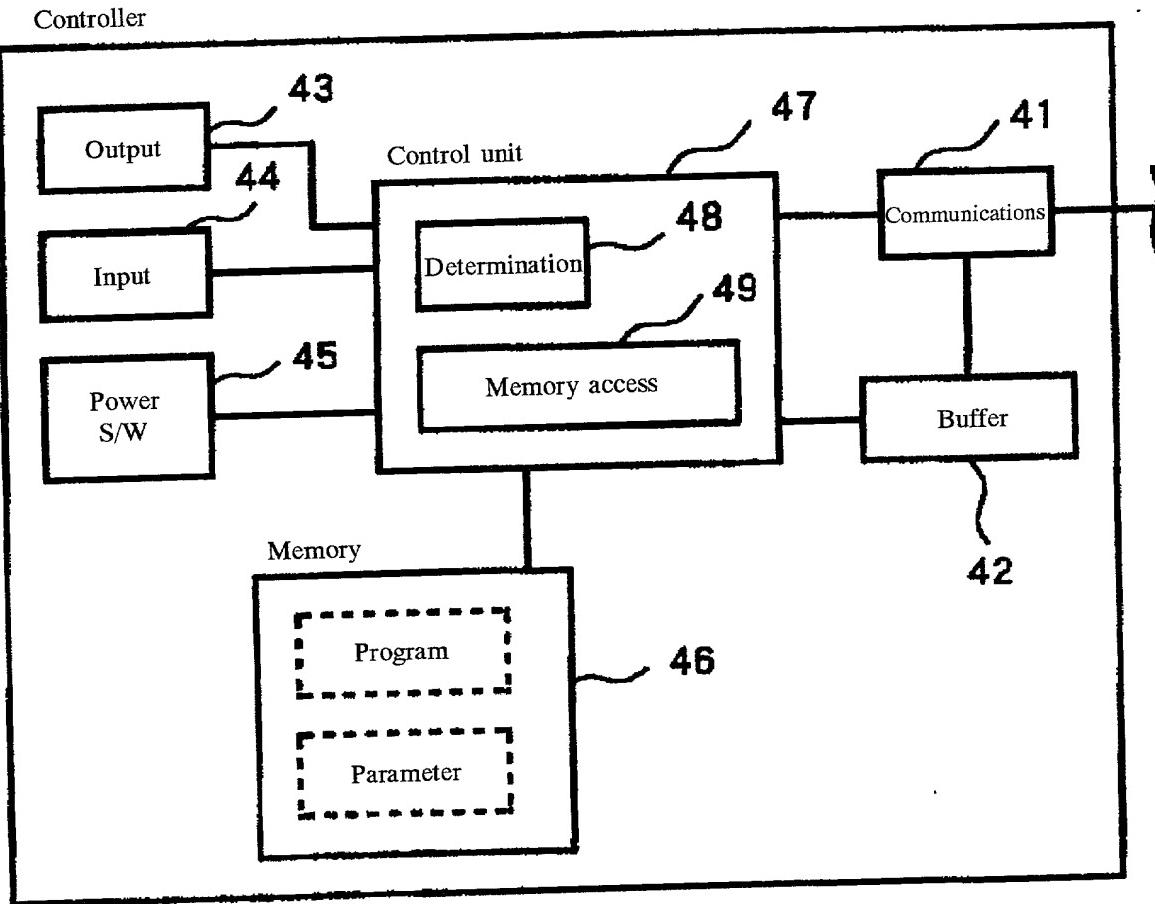


Fig.2

Administrative server (Manufacturer's PC)

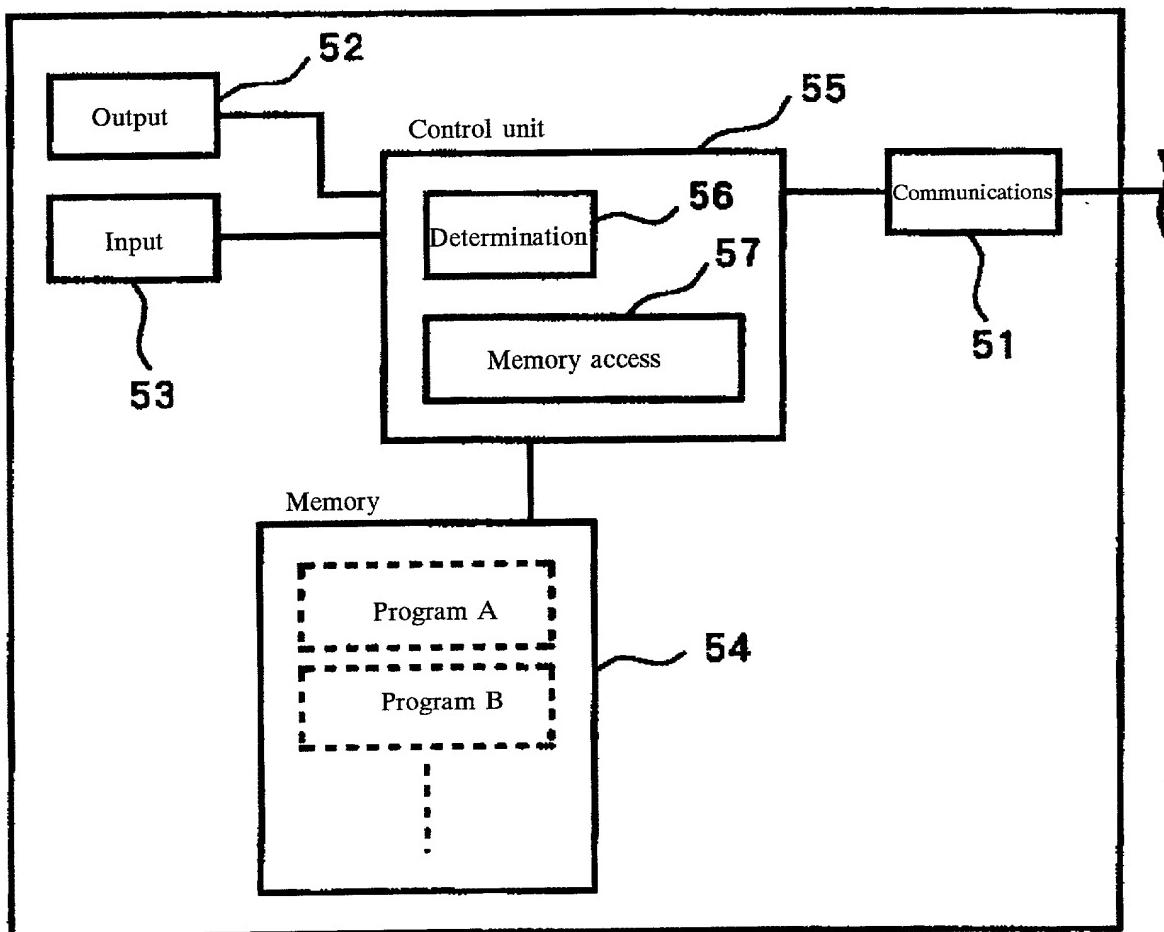


Fig.3

Operation method (Normal operation free of communications circuit error etc.)

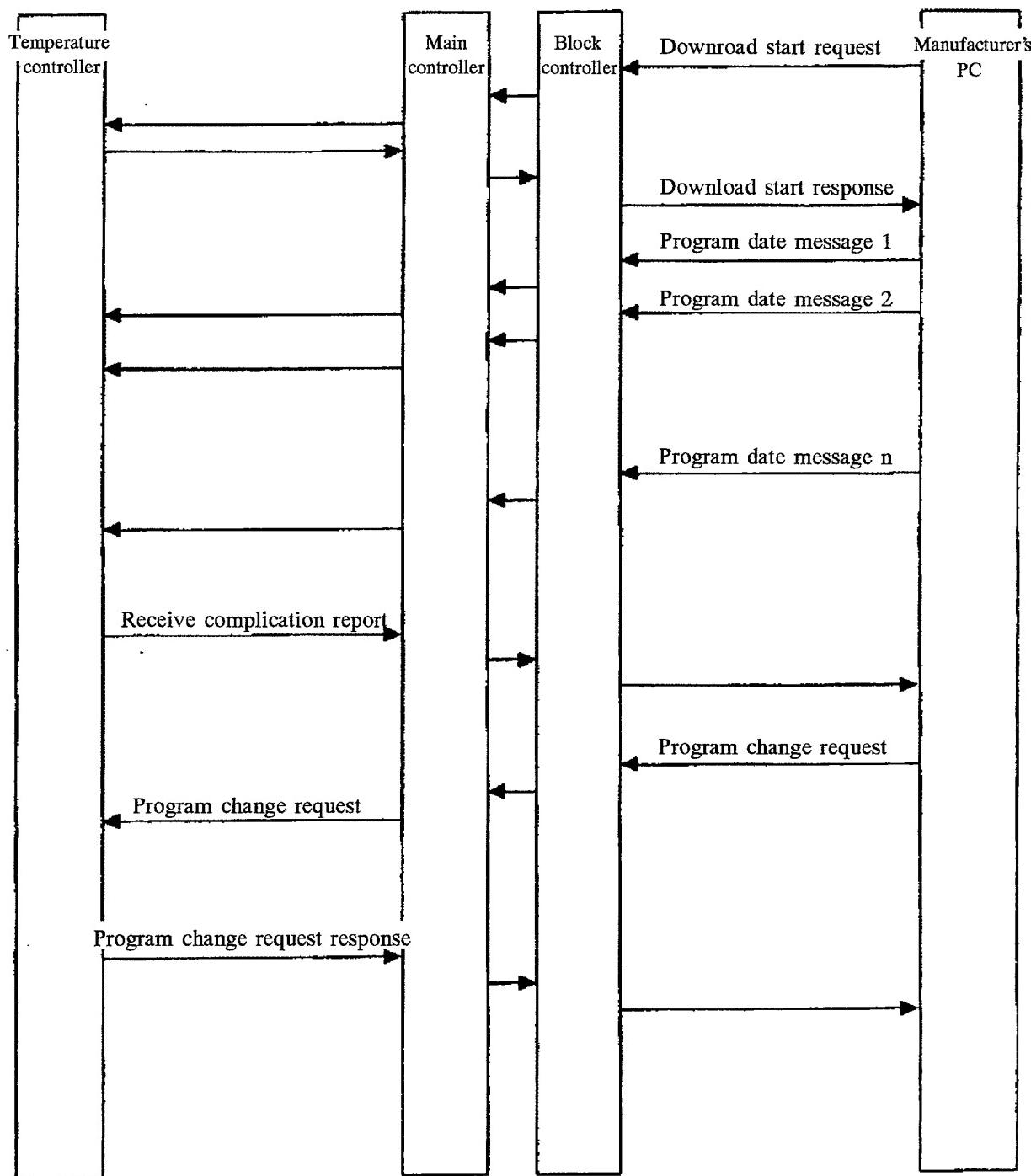


Fig.4

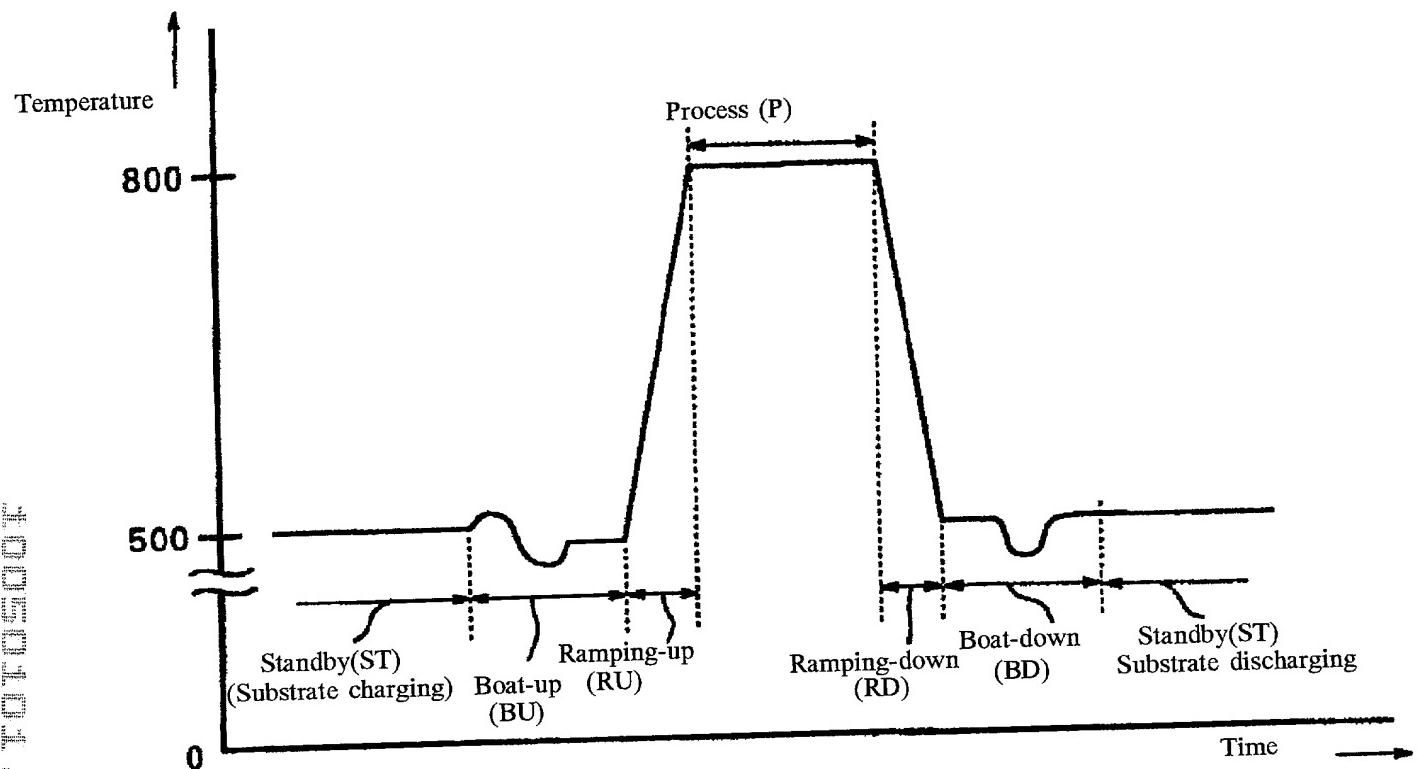


Fig.5

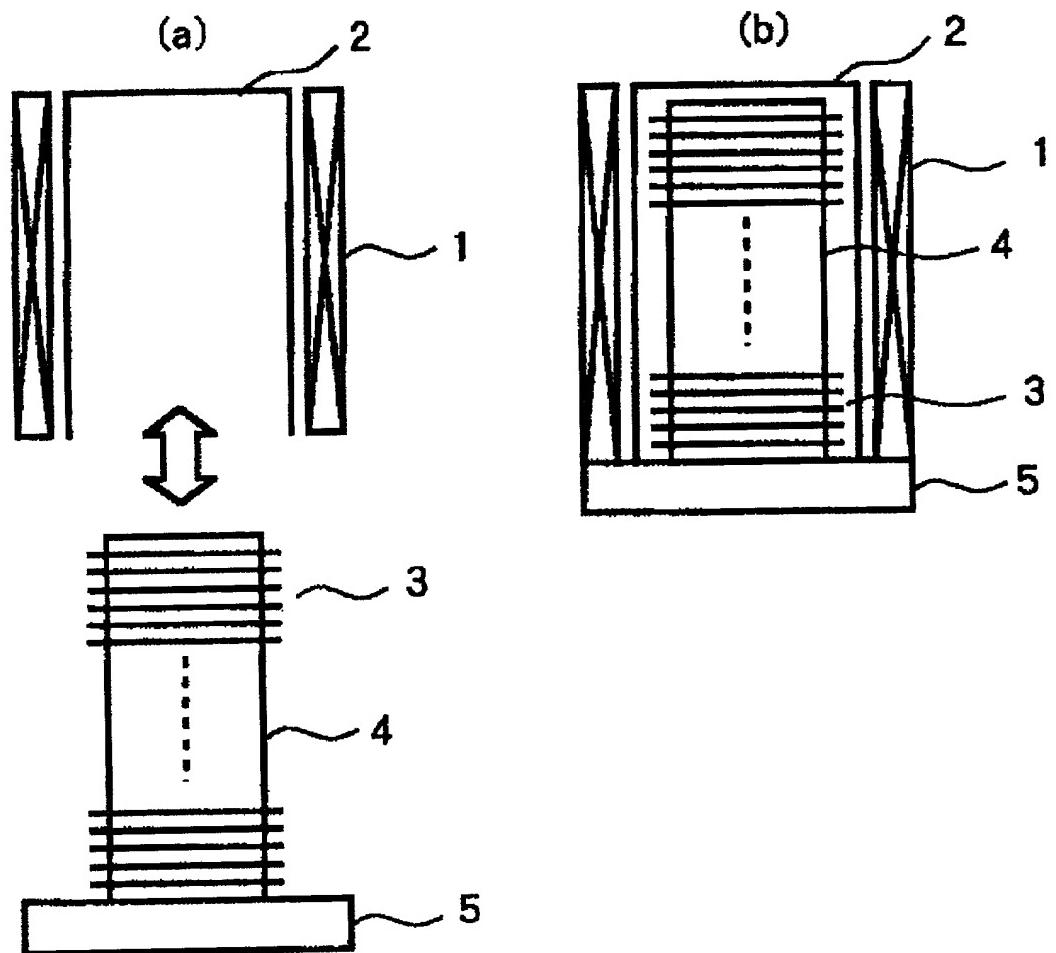


Fig.6

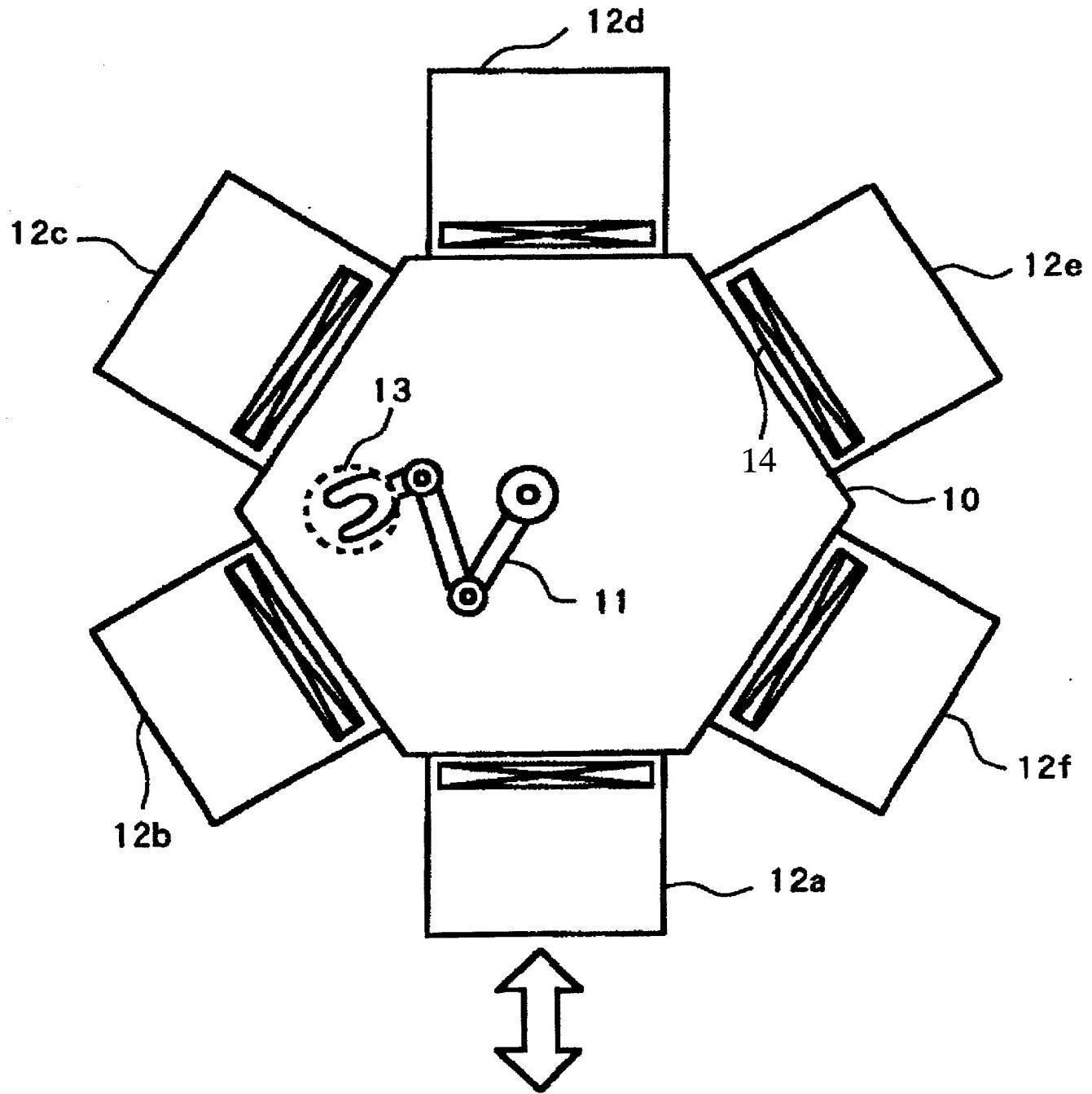
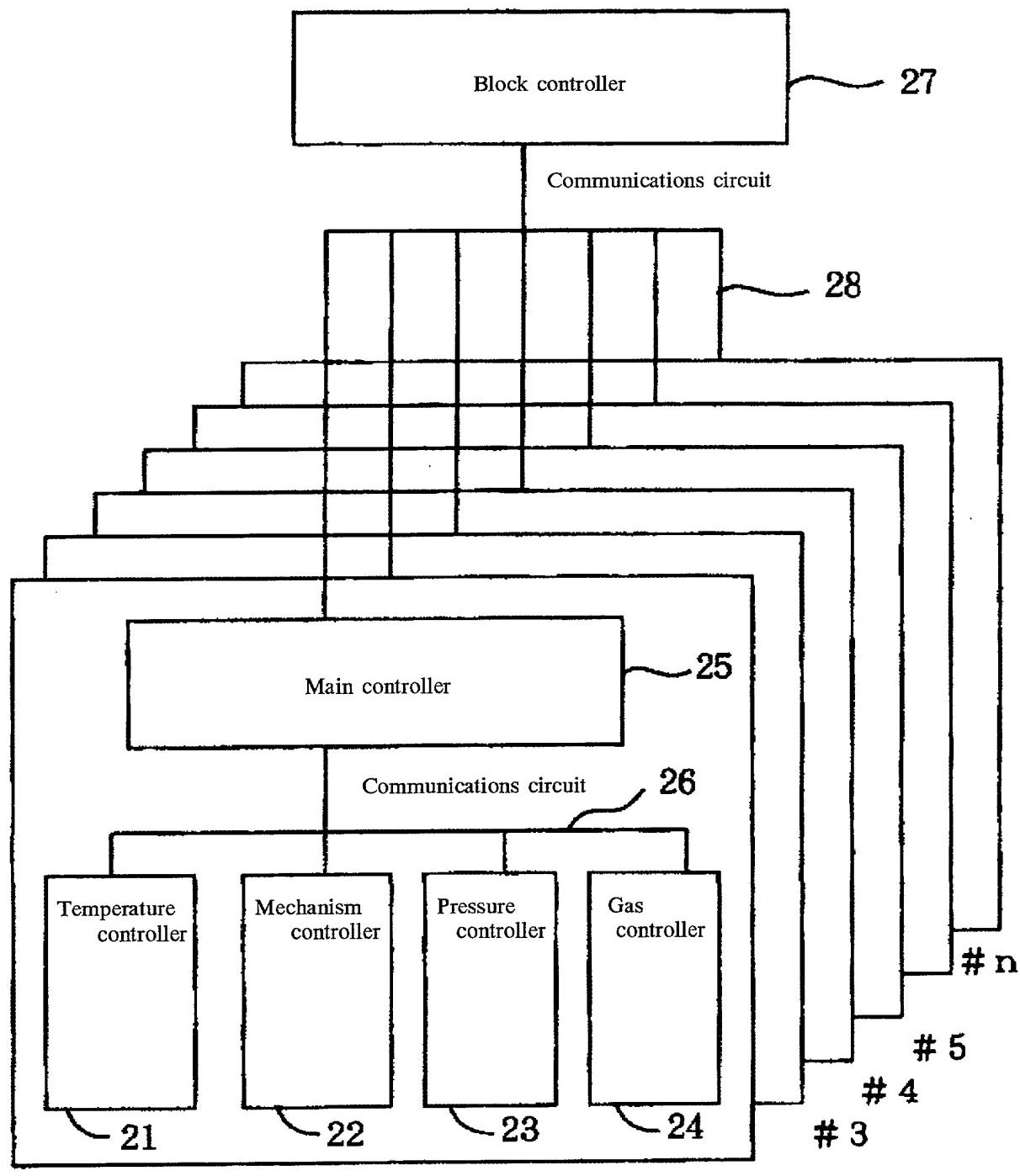


Fig.7



Semiconductor manufacturing system #1 on customer side

# 2

Fig.8